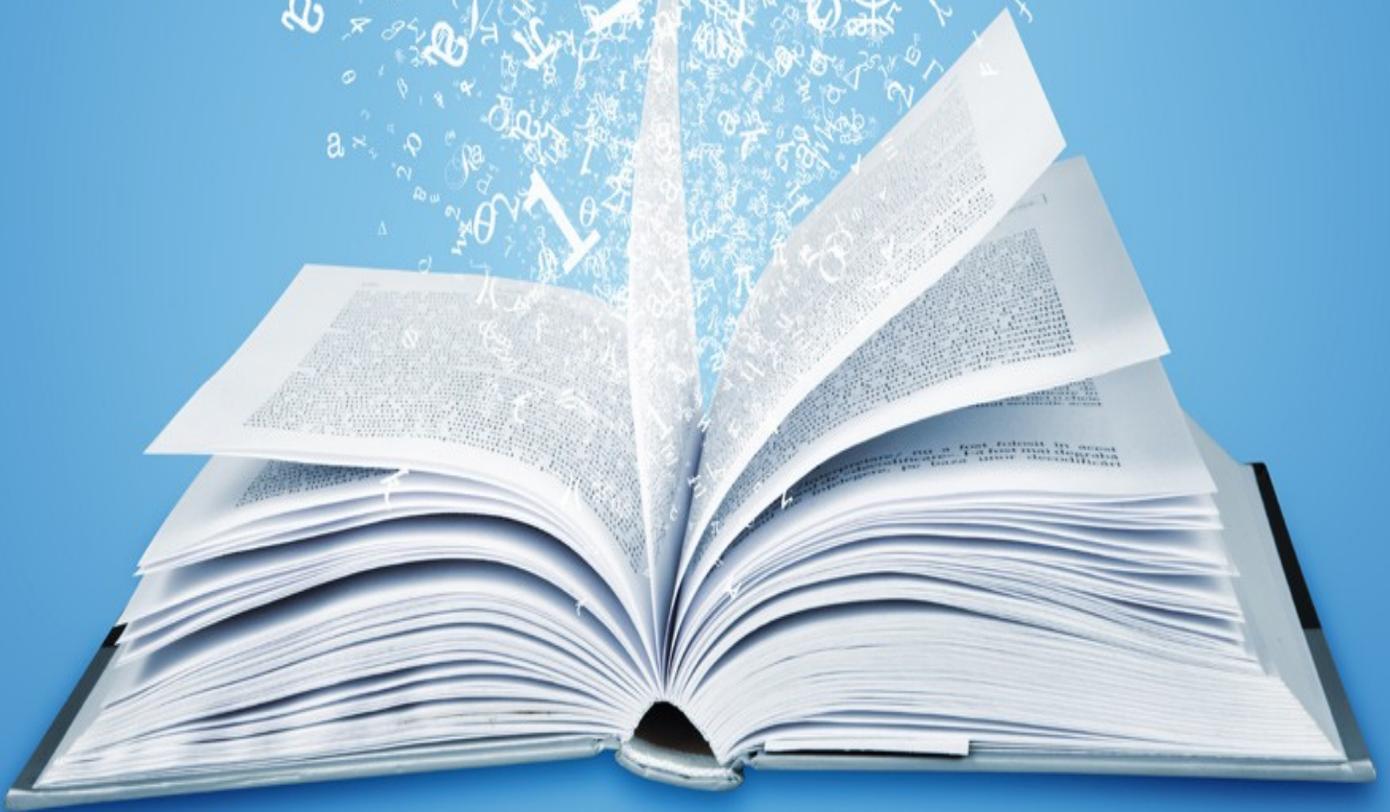




विश्वं शास्त्रे प्रतिष्ठितम्।

Reflections

ECE NEWSLETTER



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From HOD's Desk

Most of the students willing to study in the best Engineering colleges after their Higher Secondary Education for getting a better job from their campus itself. Majority of students aim to get admissions in the top Engineering Colleges in Kerala because of its good academics records and campus placements.

Despite constant updating of syllabus and core content, it is observed that soft skills are not given a significant part among the various skills set imparted to an engineering graduate. Modern HR selection process is competency based and professional students are expected to practice industry demanded skills and be ready to face the selection process. To enable students to acquire the targeted industry requirement process, the In-house training has been conducted under the leadership of Mr. Ajit Joseph, Associate professor, ECE. The students gained confidents in Basic skills, People related skills, Conceptual skills and personal skills. For a selection process, it is felt that communication skill, writing resume, positive attitude, motivation, strategic planning preparing for the interview are all important for true success. Thus the training methods are helpful to both graduates as well as the industry



Change is inevitable in the law of nature. If every change has a touch of creation, it will make things more beautiful. We too have tried to give a touch of innovation to our departments' prestigious newsletter. This time we have made an e-edition, a change from paper domain to the digital world. We are sure that readers will appreciate this venture of ours. After the first edition we, had moved from hardcore paper publication to soft-core digital publication. We think that this was a good step that can be followed by anyone. If we think about the papers' origin, is from a piece of wood which comes from a tree that makes the world green. So when we go for a bunch of papers we have to destroy a lot of trees that ultimately lead to the destruction of beautiful life giving greenery of our mother earth. But when we go for a digital publication we can avoid such a hazard. Also digital publications can be made easily available, which is also easily readable in today's digital domain. So we had decided to take this step keeping all these facts in mind. We hope all of our readers will welcome our idea and encourage us to implement such creative thoughts in the future.



Campus Placements

Our Motto

“Help the students to fulfill their dreams by providing quality placement and to assure a learning, challenging and growth oriented career”.

Sl.No	Name of the student	Name of the Company
1	Rashmi Jose	CTS
2	Krishna Vinod	CTS
3	Renjini Ravindran	CTS
4	Rashmi Jose	TCS
5	Tania James	TCS
6	Tincy James	TCS
7	Rashmi Jose	IBS
8	Sanjo. K. Mathew	BLOOMBERG
9	Rashmi Jose	VVDN
10	Tania James	SAVIANCE
11	Tincy James	SAVIANCE
12	Febin Sebastian	SAVIANCE
13	Rashmi Jose	RAZORTHINK
14	Libin P Mathew	RUBY SOFTWARE
15	Libin P Mathew	SUTHERLAND
16	Sanjo. K. Mathew	SUTHERLAND
17	Rashmi Jose	FACE
18	Swathi Gopakumar	FACE
19	Sanjo. K. Mathew	FACE
20	Tania James	FACE
21	Tincy James	FACE

In this context we are proud to say that the efforts taken during the last few years have paid off Mangalam well by having acquired a top level position in getting its students placed in various prestigious MNC's.

Rashmi Jose of ECE Dept got multiple offers from companies like **CTS, IBS, VVDN, TCS, RAZORTHINK, FACE**. On behalf of ECE Dept and faculty we congratulate her for the outstanding performance in campus recruitments. Her performance will be an inspiration to her juniors as well as her classmates, motivating them to achieve a career and to be a successful professional in life.

On behalf of ECE Dept we congratulate all the students who got placed in the reputed companies.



SAARANG 2014, The Colour Fiesta

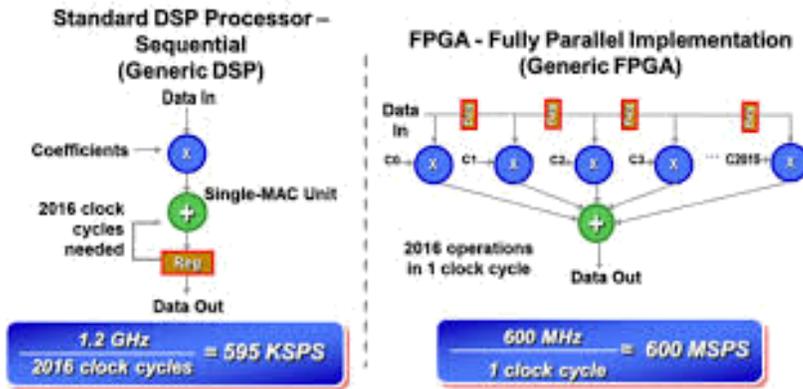


Festivals are an integral part of our Indian Culture. They bind us together with our friends and lets us enjoy every moment of life. Arts festival in colleges are such moments of joy, celebration and competition which awaken the talent inside make the mind of the youth towards a fresh state.

Mangalam College of Engineering hosted **SAARANG 2014, The Colour Fiesta** on 20th and 21st of March, 2014. The zest and zeal of students and faculty were overwhelming. The presence of cine artists Ms Miya Kurian and Mr. Dyan Sreenivasan drew large audiences. The inaugural ceremony was presided by Respected Chairman, Mr. Biju Varghese, Principal Prof. Manoj George, and Rev Father Dr.M.P.George. The event was inaugurated by well-known cine artist Ms.Miya. Among the many events on and off stage, Divo and Diva, Step n Synchro , Nostalgia, Dance choreography, Mime, drama, movie scene dubbing were crowd pullers. After the two day grand war between the for teams of Arcadians, Rhodians, Titans and the Spartans, the team Rhodians bagged the first prize. SAARANG ended with a colourful closing ceremony in which actor Mr.Dyan shared his life experiences with students. To make the colorful saga even more memorable and enjoyable for the students a DJ program was kept at the end of the day. The two days of colourful and spectacular extravaganza will be an unforgettable memory in the students mind and also in the history of Mangalam college of engineering. The college management will conduct such events in the future also to make the spirit of students to a high level. We thinks such events will make students much more energetic,creative and they will make the attitude of students to a good level.



FPGA Vs DSP Processors (FPGA-A superior choice for the solution of high level DSP problems... "Why?")



The DSP is a specialised microprocessor - typically programmed in C, perhaps with assembly code for performance. It is well suited to extremely complex mathsintensive tasks, with conditional processing. It is limited in performance by the clock rate, and the number of useful operations it can do per clock. As an example, a TMS320C6201 has two multipliers and a 200MHz clock

– so can achieve 400M multiplies per second.

In contrast, an FPGA is an uncommitted "sea of gates". The device is programmed by connecting the gates together to form multipliers, registers, adders and so forth. Using the Xilinx Core Generator this can be done at a block-diagram level. Many blocks can be very high level – ranging from a single gate to an FIR or FFT. Their performance is limited by the number of gates they have and the clock rate. Recent FPGAs have included Multipliers especially for performing DSP tasks more efficiently. – For example, a 1M-gate Virtex-II™ device has 40 multipliers that can operate at more than 100MHz. In comparison with the DSP this gives 4000M multiplies per second.

Making a Choice

There are a number of elements to the design of most signal processing systems, not least the expertise and background of the engineers working on the project. These all have an impact on the best choice of implementation. In addition, consider the resources available – in many cases, HERON I/O modules have FPGAs on board. Using these with a DSP processor may provide an ideal split.

As a rough guideline, try answering these questions:

What is the sampling rate of this part of the system? If it is more than a few MHz, FPGA is the natural choice.

Is your system already coded in C? If so, a DSP may implement it directly. It may not be the highest performance solution, but it will be quick to develop.

What is the data rate of the system? If it is more than perhaps 20-30Mbyte/second, then FPGA will handle it better.

How many conditional operations are there? If there are none, FPGA is perfect. If there are many, a software implementation may be better.

Does your system use floating point? If so, this is a factor in favour of the programmable DSP. None of the Xilinx cores support floating point today, although you can construct your own.

Are libraries available for what you want to do? Both DSP & FPGA offer libraries for basic building blocks like FIRs or FFTs. However, more complex components may not be available, and this could sway your decision to one approach or the other.

In reality, most systems are made up of many blocks. Some of those blocks are best implemented in FPGA, others in DSP. Lower sampling rates and increased complexity suit the DSP approach; higher sampling rates, especially combined with rigid, repetitive tasks, suit the FPGA.



Department Activities

Project Presentation

In the project presentation conducted by Albertian College of Engineering. Students of ECE Dept, Sherri Shaji, Davis Mathew, Renjini Ravindran, Devi bagged first prize. Students Reshmi Jose, Sabarinath, Sarath, Manu Mohan secured a second prize for their project in the same competition.



Open House

Open house were Conducted for students of department of ECE. The open house for semester 4, 6, 8 were conducted on 11th, 12th, 10th, and 7th of march. The parents analyzed the performance of their wards in various department examinations along with the subject teachers.

This helped students and their parents to get the negatives and positives of the students. So they can find their faults and can get remedy for the same.

Lab Enhancement

Our Embedded system lab is now equipped with 20 number of MP lab COMPATIBLE PIC Programmer and Debugger, from Bitsforge Ernakulam

As part of this the bitsforge people conducted a faculty development program on programming using PIC Microcontroller. The two day class conducted was very much informative to the faculties.



Faculty Development Program

As part of career enhancing it is essential for every to teacher to attend faculty development classes to update their knowledge bank with the latest cut in informations especially a teacher in the field of engineering.

Mr. reneesh C Zacharia, Mr. Ananthan P, Mr. Bibin Binu Simon, Ms. Greshma R, Ms. Anjaly V, faculty members of ECE Dept. of Mangalam attended such a program on "implementation of signal and image processing on fpga" conducted by Amrita school of engineering Coimbatore in association with XILINX CORPORATION AND Corel technologies on 10th and 11th of April.



Paper presentation

Ms.Sowmya Sadanandan, Ms.Neethu Sebastian, Ms Sethulekshmi,Ms.Sneha meryn Thomas, Ms.Shemina karim, Ms Lintu K Babu ,students (M.Tech 2012-14) ,Ms.Anna Johnson,student M.Tech(2013-2015)presented their technical papers in International Conference on Emerging Trends in Engineering & Technology (NCETET-2014) on 3rd April 2014 at Younus College of Engineering & Technology,Kollam.

New members of Mangalam Family



Ms.jency Andrews ,Asst.Professor .She joined this institution in November.

After 3years working as a teacher in MBC College Peermade,she joined our eminent team of faculty .She completed her M.Tech in VLSI Design from Sathyabhama university chennai .



Send offs

Mr.Arun Mani Asst.Professor was relieved from this institution as part of their career enhancement..He joined his career with the public sector telecom BSNL.

On Behalf of ECE Dept. We wish to place on record our heartfelt thanks to his sincere service..

WE WILL MISS YOU



Farewell 2014

Farewell is One of the best moment in the life of a student. It marks his transformation from the student life to a Professional one. At the same time its time to say goodbye to his dearest friends who made his college life a sweet memory. Another batch of students is passing out from our college. The department of ECE of Mangalam college of engineering celebrated this in an grand fashion. On this occasion respected HOD of the department Prof. Asha Panicker and faculties give messages and wishes to students. Students shared their memorizing events in mangalam with their juniors.conducted a number of games and cultural events.after the program they arranged lunch to both staffs and students.



THE END